

REMARKS

Claims 1-20 are canceled. Claims 21-28 are pending in the application, and Applicant requests examination of such pending claims.

The signature of Klaus Florian Schuegraf is missing from the filed declaration as an "uncooperative inventor". Accordingly, this application is filed with an attached granted Petition pursuant to 37 C.F.R. §1.47(a) due to an "uncooperative inventor" and a declaration signed by the cooperative inventor. The enclosed granted Petition under §1.47(a) is from the parent file (U.S. Patent Application Serial No. 09/332,271).

Respectfully submitted,

Dated: 6-4-01

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Priority Application Serial No. 09/332,271
Priority Filing Date June 11, 1999
Inventor Klaus Florian Schuegraf et al.
Assignee Micron Technology, Inc.
Priority Group Art Unit 2815
Priority Examiner E. Lee
Attorney's Docket No. MI22-1741
Title: Methods for Forming Wordlines, Transistor Gates, and Conductive Interconnects, and Wordline, Transistor Gate, and Conductive Interconnect Structures

**VERSION WITH MARKINGS TO SHOW CHANGES MADE ACCOMPANYING
PRELIMINARY AMENDMENT**

In the Specification

The replacement specification paragraphs incorporate the following amendments. Underlines indicate insertions and ~~strikeouts~~ indicate deletions.

At page 1, before the "Technical Field" section, insert the following paragraph in accordance with 37 C.F.R. § 1.121(b)(1)(ii):

--RELATED PATENT DATA

This patent resulted from a divisional application of U.S. Patent Application Serial No. 09/332,271, filed June 11, 1999.--.

Priority Appl. No. 09/332,271

In the Claims

The claims have been amended as follows. Underlines indicate insertions and ~~strikeouts~~ indicate deletions.

Please cancel claims 1-20.